

L Number	Hits	Search Text	DB	Time stamp
11	8714533	omit\$4 or avoid\$4 or skip\$4 or postpon\$4 or exclud\$4 or neglect\$4 or ignor\$4 or prevent\$4 or avert\$4 or suspen\$4 or stop\$4 or delay\$4 or hold\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 14:54
12	61141	refresh\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:19
13	1144	(omit\$4 or avoid\$4 or skip\$4 or postpon\$4 or exclud\$4 or neglect\$4 or ignor\$4 or prevent\$4 or avert\$4 or suspen\$4 or stop\$4 or delay\$4 or hold\$4) adj refresh\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:20
14	1681	(omit\$4 or avoid\$4 or skip\$4 or postpon\$4 or exclud\$4 or neglect\$4 or ignor\$4 or prevent\$4 or avert\$4 or suspen\$4 or stop\$4 or delay\$4 or hold\$4) near refresh\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:20
15	673209	register or flag	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:20
16	40430	memory adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:20
17	261	((omit\$4 or avoid\$4 or skip\$4 or postpon\$4 or exclud\$4 or neglect\$4 or ignor\$4 or prevent\$4 or avert\$4 or suspen\$4 or stop\$4 or delay\$4 or hold\$4) near refresh\$4) same (register or flag)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:21
18	88	(memory adj controller) and (((omit\$4 or avoid\$4 or skip\$4 or postpon\$4 or exclud\$4 or neglect\$4 or ignor\$4 or prevent\$4 or avert\$4 or suspen\$4 or stop\$4 or delay\$4 or hold\$4) near refresh\$4) same (register or flag))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 15:24

L Number	Hits	Search Text	DB	Time stamp
1	2	("5265231").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:31
2	40430	memory adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:39
3	1345	use adj register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:39
4	2	(memory adj controller) with (use adj register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 12:53
5	564971	register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:40
6	40684	refresh	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:40
7	419	(memory adj controller) same register same refresh	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 11:40
8	673209	register or flag	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 12:52
9	3711	refresh with (register or flag)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 12:53
10	197	(memory adj controller) same (refresh with (register or flag))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/04 12:53

EAST - [kevin's workspace.wsp:1]

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Drafts

Pending

Active

- L11: (8714533) omit\$4 or avoid\$4 or skip\$4 or postpone\$4 or exclud\$4 or neglect\$4 or ignor\$4 or p...
- L12: (61141) refresh\$4
- L13: (1144) 11 adj 12
- L14: (1681) 11 near 12
- L15: (673209) register or flag
- L16: (40430) memory adj controller
- L17: (261) 14 same 15
- L18: (88) 16 and 17**

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search List Browse Queue Clear

DBs: USPAT, US-PGPUB, EPO, JPO, DERWENT, IBM, TDB

Default operator: OR

☒ Elurels

☒ Highlight all hit terms initially

16 and 17

BRS form IS&R form Hls Details

Ready NUM



- ☐ Drafts
- ☐ Pending
- ☒ Active
  - ☒ L1: (2) ("5265231").PN.
  - ☒ L2: (40430) memory adj controller
  - ☒ L3: (1345) use adj register
  - ☒ L4: (2) 2 with 3
  - ☒ L5: (564971) register
  - ☒ L6: (40684) refresh
  - ☒ L7: (419) 2 same 5 same 6
  - ☒ L8: (673209) register or flag
  - ☒ L9: (3711) 6 with 8
  - ☒ L10: (197) 2 same 9
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (0)
- ☐ UDC
- ☐ Queue
- ☐ Trash

Search: List Browse Queue Clear

DBs: USPAT, US-PGPUB, EPO, JPO, DERWENT, IBM, TDB

Default operator: OR

☒ Plurals☒ Highlight all hit terms initially

2 same 9